

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

MICHAEL A. ROTHMAN, ET AL.

Application No.:

Filed:

For: **Method and Apparatus for Enabling
Platform Configuration**

Art Group:

Examiner:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s). Copies of the references cited on PTO/SB/08 are enclosed herewith.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: February 09, 2004



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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



Derek S. Watson

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	
				Filing Date	
				First Named Inventor Michael A. Rothman	
				Art Unit	
				Examiner Name	
Sheet	2	of	2	Attorney Docket Number 42P18513	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	3	Nonpublished US Patent Application, "Technique for Reconstituting a Pre-Boot Firmware Environment After Launch of an Operating System", 10/324,263, filed 12/18/02 to Zimmer et al.	
	4	Intel E7500 Chipset Datasheet, Intel E5700 Memory Controller Hub (MCH) February 2002, Document Number 290730-001	
	5	Mark Doran, "From Research and Development at Intel: Beyond the BIOS", Intel Corporation, February 19th, 2003	
	6	Insyde Software, "Keyboard Controller Firmware for Hitachi IKAP Family", Information Appliance Products, www.insyde.com/solutions/ia/ikap.com, February 9, 2004	
	7	E-mail discussion thread, RE: Missing Interrupts, March 19, 2003	

Examiner Signature	Date Considered
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

*Applicant's unique citation designation number. *Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.
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